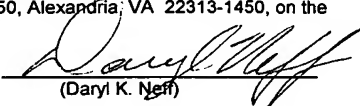


I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: November 20, 2003 Signature: 

(Daryl K. Neff)

Docket No.: INTECH 3.0-090
03 P 50510 US
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Peter Poechmueller

Serial No.: 10/618,333

Group Art Unit: 2818

Filed: July 11, 2003

Examiner: Not Yet Assigned

For: **STRUCTURE AND METHOD OF
MULTIPLEXING BITLINE SIGNALS
WITHIN A MEMORY ARRAY**

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed herewith are seven sheets of formal drawings for filing in connection with the above-identified patent application. The formal drawings are submitted in order to correct the informalities listed in form PTO-948.

In the event there are any fees due or owing in connection with this matter, please charge same to our Deposit Account No. 12-1095.

Dated: November 20, 2003

Respectfully submitted,

By 
Daryl K. Neff

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